



THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant:** Yoshiyuki Yanagisawa et al. **Atty. Docket No.** 075834.00085  
**Serial No.:** 09/876,290 **Group Art Unit:** 2827  
**Filed:** June 7, 2001 **Examiner:** David E. Graybill  
**Invention:** "ASSEMBLY JIG AND MANUFACTURING METHOD OF  
MULTILAYER SEMICONDUCTOR DEVICE"

**AMENDMENT B**

Assistant Commissioner of Patents  
Washington, D.C. 20231

S I R:

In response to the Office Action dated November 6, 2002, please amend the  
application as follows:

**PETITION FOR EXTENSION OF TIME**

Applicant hereby petitions for a three-month extension of time to respond to the  
outstanding Office Action under 37 C.F.R. §1.136(a). The time to respond is thus extended  
to May 6, 2003. Applicant has also included a check in the amount of \$930.00 as payment  
of the required fee set forth in 37 C.F.R. §1.17(a).